



Approval

Model No.:

Parts Code: V33A00031200

Customer : GA DATE : 13. Jan. 2010

SAMSUNG TFT-LCD

MODEL: LTA460HJ05

NOTE:	

Customer's Ap	oproval
SIGNATURE	DATE

APPROVED BY	DATE
Kyunghwan Ko	13. Jan. 2010
PREPARED BY	DATE
Jeong-Geun Han	13. Jan. 2010

LCD Business

Samsung Electronics Co., LTD.

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* Revision History

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Date	Rev. No	Page	Summary
Dec 10, 2009	000	all	First Issued
Dec 22, 2009	000	22	Outline Dimension Rear Updated
Dec 30, 2009	001	10	Operating Life Time : Typical 50,000 hr → Typical 30,000 hr
Jan 13,	000	4, 21	Module Size D _{MAX} : 29.9 → 30.9mm
2010	002	10	Additional Note (2) : SLED Life time Test condition.

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General Description

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Description

LTA460HJ05 is a color active matrix liquid crystal display (LCD) that uses amorphous silicon TFT(Thin Film Transistor) as switching components. This model is composed of a TFT LCD panel, a driver circuit and a back light unit. The resolution of a 46.0" is 1920 x 1080 and this model can display up to 1.07 Billion colors with wide viewing angle of 89° or higher in all directions. This panel is intended to support applications to provide a excellent performance for Flat Panel Display such as Home-alone Multimedia TFT-LCD TV and High Definition TV

Features

- RoHS compliance (Pb-free)
- High contrast ratio & aperture ratio with wide color gamut
- SPVA(Super Patterned Vertical Align) mode
- Wide viewing angle (±178°)
- High speed response (& Natural Motion (DFR: Double Frame Rate))
- FHD resolution (16:9)
- Low Power consumption
- Edge Type LED (Light Emitted Diode) BLU
- DE (Data Enable) mode
- 4ch LVDS (Low Voltage Differential Signaling) interface (4pixel/clock)

General Information

Items	Specification	Unit	Note
Module Size	1076.5(H _{TYP}) x 634.7(V _{TYP})	mm	±1.0mm
Module Size	30.9 (D _{MAX})] ''"''	
Weight	11,300 (Max)	g	
Pixel Pitch	0.53025(H) x 0.53025(W)	mm	
Active Display Area	1018.08(H) x 572.67(V)	mm	
Surface Treatment	Haze 0 %, Hard Coating 3H		
Display Colors	8 bit + FRC – 1.07 Billion	colors	
Number of Pixels	1920 x 1080	pixel	
Pixel Arrangement	RGB vertical stripe		
Display Mode	Normally Black		
Luminance of White	450 (Typ.)	cd/m ²	

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1. Absolute Maximum Ratings

If the condition exceeds maximum ratings, it can cause malfunction or unrecoverable damage to the device.

Item	Syr	mbol	Min.	Max.	Unit	Note
Power Supply Voltage	V	' _{DD}	GND-0.5	13.2	V	(1)
Dimming Control	Max	. Lum	-	5	V	(1)
Storage temperature	T,	STG	-20	60	$^{\circ}$	(2)
Operating temperature	T,	OPR	0	50	°C	(2)
Surface temperature	T,	SUR	0	60	Ç	(3)
Shock (non - operating)	9	X,Y	-	40	G	(4)
Shock (non - operating)	S _{NOP}	Z	-	30	9	(4)
Vibration (non - operating)	V	NOP	-	1.5	G	(5)

Note (1) Ta= 25 \pm 2 °C

- (2) Temperature and relative humidity range are shown in the figure below.
 - a. 90 % RH Max. (Ta \leq 39 °C)
 - b. Relative Humidity is 90% or less. (Ta > 39 °C)
 - c. No condensation
- (3) Although abnormal visual problems can be occurred in T_{SUR} range, the polarizer is not damaged in this range.
- (4) 11ms, sine wave, one time for $\pm X$, $\pm Y$, $\pm Z$ axis
- (5) 10-300 Hz, Sweep rate 10min, 30min for X,Y,Z axis

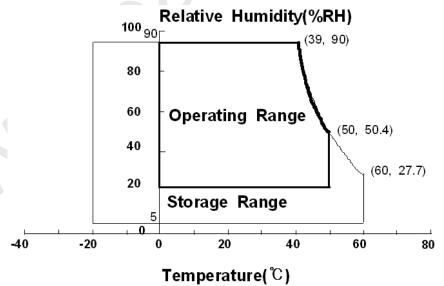


Fig. Temperature and Relative humidity range

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2. Optical Characteristics

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The optical characteristics should be measured in a dark room or equivalent. Measuring equipment: TOPCON RD-80S, TOPCON SR-3, ELDIM EZ-Contrast

(Ta = 25 \pm 2°C, VDD=12V, fv= 120Hz, f_{DCLK}= 297.0MHz, LED Current = 95mA)

	(, VDD 12V,		-, 'DCLK		., LLD Ou	
Item		Symbol	Condition	Min.	Тур.	Max.	Unit	Note
Contrast R (Center of so		C/R		4000	5000	ı		(1) SR-3
Response Time	G-to-G	Tg		ı	6	-	msec	(3) RD-80S
Luminance of (Center of so		Y _L		360	450	1	cd/m ²	(4) SR-3
	Red	Rx	Normal		0.622			
	Red	Ry	q L,R =0 q U,D =0		0.331			
	Green	Gx	q 0,D =0		0.305			
Color Chromaticity	Green	Gy	Viewing	TYP.	0.631	TYP.		(5),(6)
(CIE 1931)	Blue	Bx	Angle	-0.03	0.154	+0.03		SR-3
	Dide	Ву			0.050			
	White	Wx			0.280			
	VVIIILE	Wy			0.290			
Color Gar	mut	-		-	72	-	%	(5)
Color Tempe	erature	-		-	10,000	-	K	SR-3
	Llow	q_L		75	89	-		
Viewing	Hor.	q_R	C/D>10	75	89	-	D	(6)
Angle	Vor	q _U	C/R≥10	75	89	-	Degree	EZ-Contrast
	Ver.	q_D		75	89	-		
White Brigh Uniformi (9 Point	ty	B _{uni}		-	-	25	%	(2) SR-3

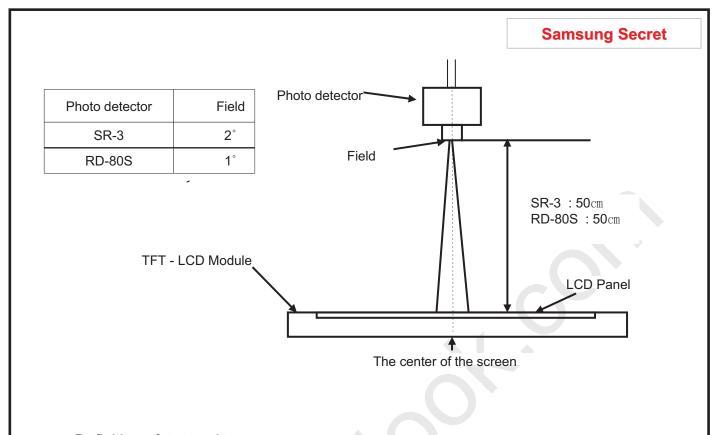
- Test Equipment Setup

The measurement should be executed in a stable, windless and dark room between 40min and 60min after lighting the back light at the given temperature for stabilization of the back light. This should be measured in the center of screen.

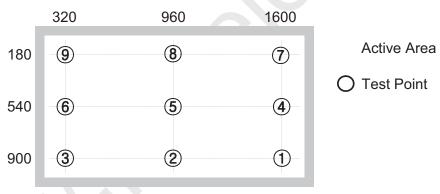
Environment condition : Ta = 25 ± 2 °C

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- Definition of test point



Note (1) Definition of Contrast Ratio (C/R)

: Ratio of gray max (Gmax) & gray min (Gmin) at the center point ⑤ of the panel

$$C/R = \frac{G \max}{G \min}$$

Gmax: Luminance with all pixels white Gmin: Luminance with all pixels black

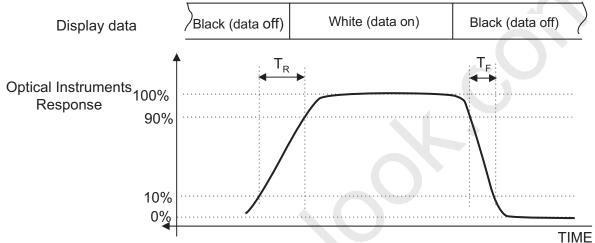
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Note (2) Definition of 9 points brightness uniformity (Test pattern : Full White)

$$Buni = 100* \frac{(B \max - B \min)}{B \max}$$

Bmax : Maximum brightness Bmin : Minimum brightness

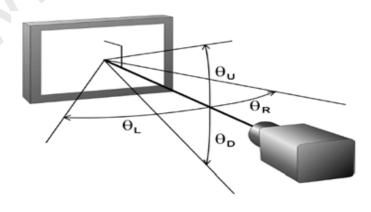
Note (3) Definition of Response time : Sum of Tr, Tf



Note (4) Definition of Luminance of White: Luminance of white at center point ⑤

Note (5) Definition of Color Chromaticity (CIE 1931)
Color coordinate of Red, Green, Blue & White at center point ⑤

Note (6) Definition of Viewing Angle : Viewing angle range (C/R ≥10)



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3. Electrical Characteristics

3.1 TFT LCD Module

The connector for display data & timing signal should be connected.

Ta = 25° C \pm 2 $^{\circ}$ C

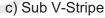
Item		Symbol	Min.	Тур.	Max.	Unit	Note
Voltage of P	Voltage of Power Supply		10.8	12.0	13.2	V	(1)
Current of (a) Black			-	526	-	mA	
Power	(b) White	I _{DD}	-	509	-	mA	(2),(3)
Supply	(c) Sub V-Stripe		-	1071	1200	mA	
Vsync Frequ	iency	f _V	95.0	120.0	125.0	Hz	
Hsync Frequ	Hsync Frequency		120.0	135.0	140.0	kHz	
Main Frequency		f _{DCLK}	260.0	297.0	305.0	MHz	
Rush Current		I _{RUSH}	-	-1	6	А	(4)

Note (1) The ripple voltage should be controlled under 10% of V_{DD}.

- (2) fV=120Hz, fDCLK=297.0MHz, $V_{DD}=12.0V$, DC Current.
- (3) Power dissipation check pattern (LCD Module only)



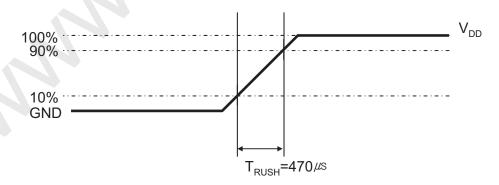








(4) Measurement Conditions



Rush Current I_{RUSH} can be measured when T_{RUSH} . is 470 μ s.

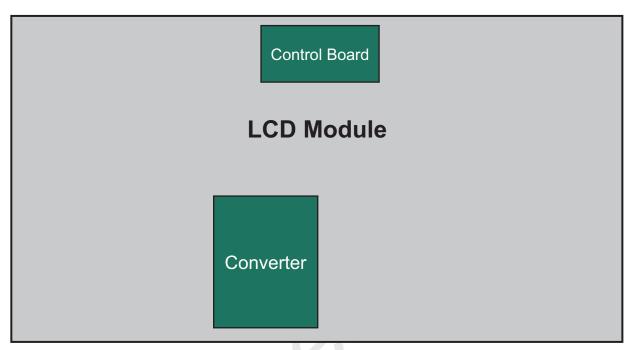
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3.2 Back Light Unit

The back light unit contains Edge type White LEDs (Light Emitting Diode)

Ta=25 \pm 2°C



Item	Symbol	Min.	Тур.	Max.	Unit	Note	
Operating Life Time	Hr	-	30,000	-	Hour	(1),(2)	

Note (1) It is defined as the time to take until the brightness reduces to 50% of its original value. [Operating condition : $Ta = 25\pm2^{\circ}C$, For LED Package only.]

(2) SLED Test Condition: 120mA / 2000hr / Tj = 75'C

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Global LCD Panel Exchange Center

3.3 Inverter Input Condition & Specification

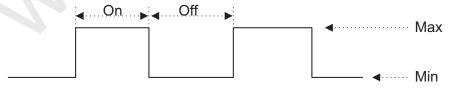
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Items	Symbol	Conditions	Sp	ecificatio	ns	Unit	Note
items	Symbol	Conditions	Min.	Тур.	Max.	Offic	Note
Input Voltage	Vin	-	22	24	26	V	Ta=25±2 °C (2)
Input	I _{OVER}	Vin=24.0V	-	3.87	4.85	^	(1)
Current	I _{SAT}	Vdim =3.3V	-	3.60	3.91	A	
LED Current	I _{O,MAX}	Vdim =3.3 V	90	95	100	mArms	(2)
Backlight	ON	Vin=24.0 V	2.4	-	5.25	\(\frac{1}{2}\)	(2)
On/Off	OFF	Vin=24.0 V	0	-	0.8	V	(3)
Dimming	.,	Max Lum	3.3	-	-	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	(0)
Control	V _{DIM}	Min. Lum	-	-	0	V	(3)
PWM	.,	Max	3.0	-	5.0		(0)
Voltage	V_{PWM}	Min	0.0		0.4	V	(3)
PWM Frequency	F _{PWM}	Vin=24.0 V	95	-	185	Hz	
PWM Duty	Duty	Vin=24.0 V	0	-	100	%	(4)

Note) Power Consumption is measured when 450 [cd/m] of luminance which is the typical luminance.

Lamp Current is measured at the point before Lamp.

- (1) Max Value of the Power Consumption is measured during initial turn on time* of the backlight
- (2) Max Value of the Power Consumption is measured after 60 min warm-up.
- (3) The ripple voltage should be controlled under 10% of Input Signal
- (4) Duty = On/(On+Off) * 100
- * Initial turn-on time: From 0sec to 60min after turn-on



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4. Input Terminal Pin Assignment

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4.1.1 Input Signal & Power

Connector: FI-RE41S-HF (JAE/UJU)

Pin	Description		Pin	Symbol	Description
1	Vdd(12V)		21		Rx1[3]P
2	Vdd(12V)		22		Rx1[4]N
3		Vdd(12V)	23		Rx1[4]P
4		Vdd(12V)	24		GND
5		Vdd(12V)	25		Rx3[0]N
6	N	o Connection	26		Rx3[0]P
7		GND	27		Rx3[1]N
8		GND	28		Rx3[1]P
9		GND	29	ODD LVDS	Rx3[2]N
10		Rx1[0]N	30	SIGNAL	Rx3[2]P
11		Rx1[0]P	31		GND
12		Rx1[1]N	32		Rx3CLK-
13		Rx1[1]P	33		Rx3CLK+
14		Rx1[2]N	34		GND
15	ODD LVDS SIGNAL	Rx1[2]P	35		Rx3[3]N
16		GND	36		Rx3[3]P
17		Rx1CLK-	37		Rx3[4]N
18	N	Rx1CLK+			Rx3[4]P
19		GND	39		GND
20		Rx1[3]N	40	N	lo Connection
			41	N	lo Connection

Note) No Connection: This PINS are only used for SAMSUNG internal using.

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4.1.2 Input Signal & Power

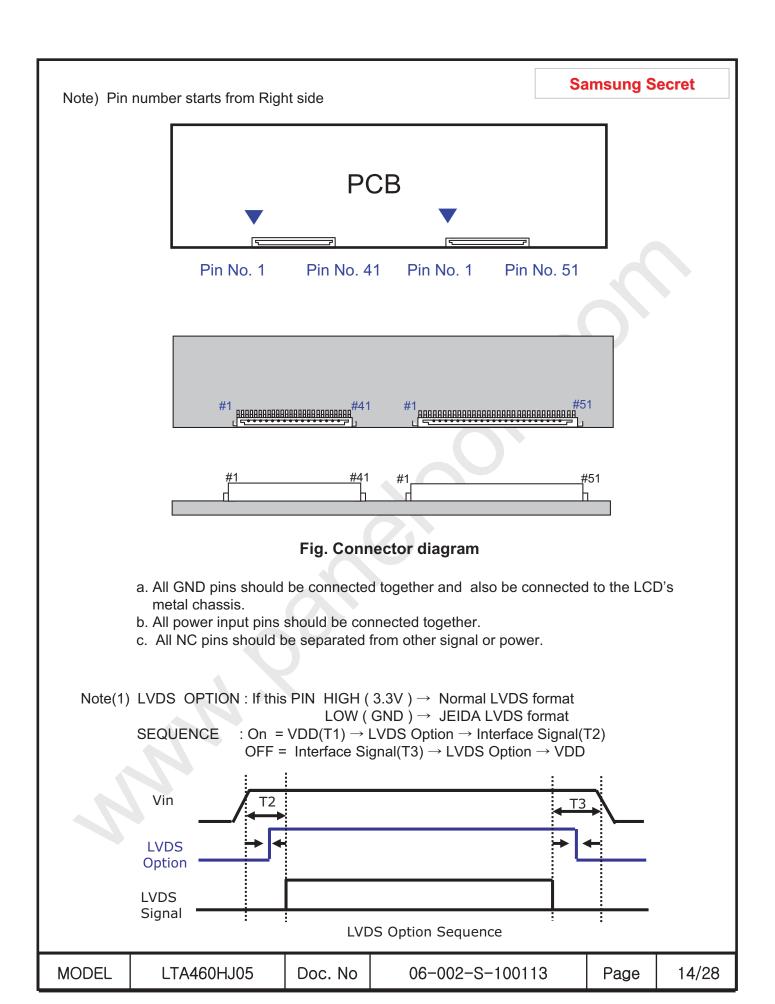
Connector	. г	.I-KE2	1 3 -пг	(JAE/UJU)

Pin		Description	Pin		Description	
1		Vdd(12V)	26		Rx4[0]P	
2		Vdd(12V)	27		Rx4[1]N	
3	Vdd(12V)		28		Rx4[1]P	
4		Vdd(12V)	29		Rx4[2]N	
5		Vdd(12V)	30		Rx4[2]P	
6		No Connection	31	EVEN	GND	
7		GND	32	LVDS	Rx4CLK-	
8		GND	33	SIGNAL	Rx4CLK+	
9		GND	34		GND	
10		Rx2[0]N	35		Rx4[3]N	
11		Rx2[0]P	36		Rx4[3]P	
12		Rx2[1]N	37		Rx4[4]N	
13		Rx2[1]P	38		Rx4[4]P	
14		Rx2[2]N	39		GND	
15		Rx2[2]P	40		No Connection	
16		GND	41		No Connection	
17	EVEN	Rx2CLK-	42		No Connection	
18	LVDS SIGNAL	Rx2CLK+	43		No Connection	
19		GND	44		No Connection	
20		Rx2[3]N	45	Ľ	VDS Option * Note(1)	
21		Rx2[3]P	46		No Connection	
22		Rx2[4]N	47		No Connection	
23		Rx2[4]P	48		No Connection	
24		GND	49		No Connection	
25		Rx4[0]N	50		No Connection	
			51		No Connection	

NOTE) No connection: This Pins are only used for SAMSUNG internal using

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4.2. Inverter Input Pin Configuration

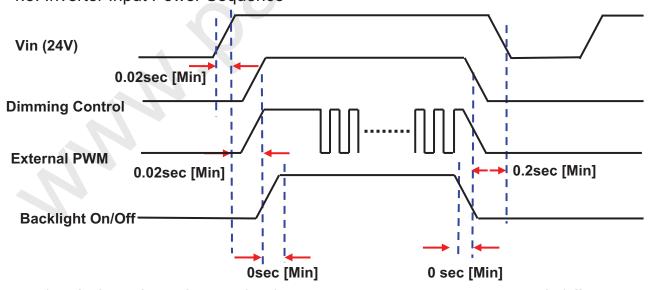
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Connector: Yeon-ho, 20022WR-14B1

Pin No.	Pin Configuration(FUNCTION)
PIII NO.	Master
1	24 V
2	24 V
3	24 V
4	24 V
5	24 V
6	GND
7	GND
8	GND
9	GND
10	GND
11	Error Out
12	Backlight On /Off [ON:2.4 - 5.25 V, OFF: 0 - 0.8 V]
13	Dimming Control [0V:Min, 3.3V:Max] *Note(1)
14	External PWM [0~100%] *Note(1)

Note(1) If use Dimming Control, Pin 14 Must be N.C If use External PWM, Pin 13 Must be N.C

4.3. Inverter Input Power Sequence



Note) SEQUENCE: On = Vin(24V) > Internal or External PWN ≥ Backlight On/off OFF = Backlight On/Off ≥ Internal or External PWN > Vin(24V)

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4.4 LVDS Interface

- LVDS Receiver : Tcon (merged)

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Data	Format	(JEIDA & Nor	mal)						
		LVDS p	in	JEIDA -DATA	N	lormal -DA	TA		
		TxIN/RxO	UT0	R4		R0			
		TxIN/RxO	UT1	R5		R1			
		TxIN/RxO	UT2	R6		R2			
TxOUT/F	RxIN0	TxIN/RxO	UT3	R7		R3			
		TxIN/RxO	UT4	R8		R4			
		TxIN/RxO	UT6	R9		R5			
		TxIN/RxO	UT7	G4		G0			
		TxIN/RxO	UT8	G5		G1			
		TxIN/RxO	UT9	G6		G2			
		TxIN/RxOl	JT12	G7		G3			
TxOUT/F	RxIN1	TxIN/RxOl	JT13	G8		G4			
		TxIN/RxOl	JT14	G9		G5			
		TxIN/RxOl	JT15	B4		В0			
		TxIN/RxOl	JT18	B5		B1			
		TxIN/RxOl	JT19	B6		B2			
		TxIN/RxOl	JT20	B7 B3					
		TxIN/RxOl	JT21	B8		B4			
TxOUT/F	RxIN2	TxIN/RxOl	JT22	B9		B5			
		TxIN/RxOl	JT24	HSYNC		HSYNC			
		TxIN/RxOl	JT25	VSYNC		VSYNC			
		TxIN/RxOl	JT26	DEN		DEN			
		TxIN/RxOl	JT27	R2		R6			
		TxIN/RxO	UT5	R3		R7			
		TxIN/RxOl	JT10	G2		G6			
TxOUT/F	RxIN3	TxIN/RxOl	JT11	G3		G7			
		TxIN/RxOl	JT16	B2		В6			
		TxIN/RxOl	JT17	В3		B7			
		TxIN/RxOl	JT23	RESERVED		RESERVE	D		
		TxIN/RxOl	JT28	R0		R8			
		TxIN/RxOl	JT29	R1		R9			
		TxIN/RxOl	JT30	G0		G8			
TxOUT/F	RxIN4	TxIN/RxOl	JT31	G1		G 9			
		TxIN/RxOl	JT32	В0		В8			
		TxIN/RxOl	JT33	B1		В9			
		TxIN/RxOl	JT34	RESERVED		RESERVE	D		
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4.5 Input Signals, Basic Display Colors and Gray Scale of Each Color

	DIOE:														DA	TA S	SIGN	IAL														GRAY
COLOR	DISPLAY (8bit)					RI	ΞD									GRI	EEN									BL	UE					SCAL LEVE
		R0	R1	R2	R3	R4	R5	R6	R7	R8	R9	G0	G1	G2	G3	G4	G5	G6	G7	G8	G9	В0	B1	B2	В3	B4	B5	В6	В7	В8	В9	
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	-
	GREEN	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	-
BASIC	CYAN	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
COLOR	RED	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	MAGENTA	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	-
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	-
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0
		1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1
	DARK	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2
GRAY SCALE	1	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	-			:	:	:	:	:	:	:	:	:	R3-
OF RED	\downarrow	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	i		••		:	:	:	:	:	:	:	:	<u>:</u>	:	:	R10
	LIGHT	1	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R10
		0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R102
	RED	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R102
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0
		0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G1
	DARK	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G2
GRAY SCALE	1		:		:	:	:		:	(:,							:	:		:		:	:		:	:	:	:	:	:	:	G3
OF GREEN	↓	:	:		:	:	:		67	:		7 :	:		• •	:	:	:		:	:	:	:	:	:	:	:	:	:	:	:	G102
	LIGHT	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	G102
		0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	G102
	GREEN	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	G102
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	В0
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	В1
	DARK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	B2
GRAY SCALE	1	1		,	:	:	:	:		:	:		:					:					:	:		:	:	:	:	:	:	B3-
OF BLUE	SCALE OF	:		:	:	:	:	:		:	:	:	:			:		:		:	:	:	:	:		:	:	:	:	:	:	B102
	LIGHT	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	1	1	B102
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	B102
[BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	B102

Note) Definition of Gray:

Rn: Red Gray, Gn: Green Gray, Bn: Blue Gray (n = Gray level)

Input Signal: 0 = Low level voltage, 1 = High level voltage

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5. Interface Timing

5.1 Timing Parameters (DE mode)

SIGNAL	ITEM	SYMBOL	MIN.	TYP.	MAX.	Unit	NOTE
Clock		1/T _C	260.0	297.0	305.0	MHz	-
Hsync	Frequency	F _H	120.0	135.0	140.0	KHz	-
Vsync		F _V	95.0	120.0	125.0	Hz	-
Vertical	Active Display Vertical Period		-	1080	-(Lines	-
Display Term	Vertical Total	T _v	1092	1125	1380	Lines	-
Horizontal	Active Display Period	T _{HD}	\C	1920	-	Clocks	-
Display Term	Horizontal Total	Тн	2090	2200	2350	clocks	-

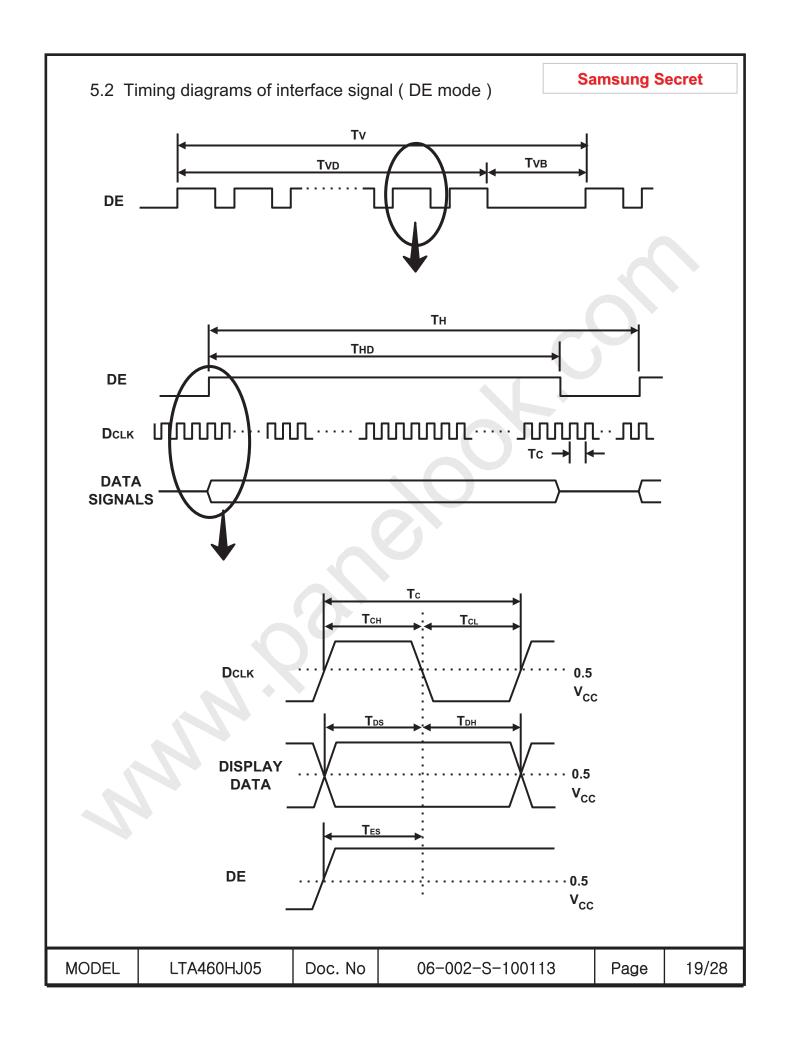
Note) This product is DE mode. But the Hsync & Vsync signal must be inputted

(1) Test Point: TTL control signal and CLK at LVDS Tx input terminal in system

(2) Internal VDD = 3.3V

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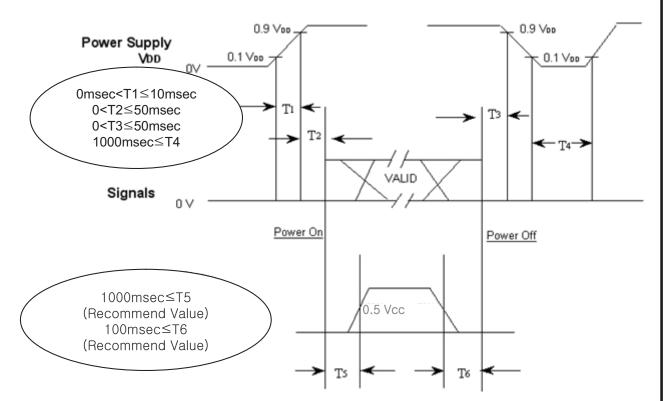




5.3 Power ON/OFF Sequence

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To prevent a latch-up or DC operation of the LCD Module, the power on/off sequence should be as the diagram below.



T1 : V_{DD} rising time from 10% to 90%

T2 : The time from V_{DD} to valid data at power ON.

T3 : The time from valid data off to $V_{\rm DD}$ off at power Off.

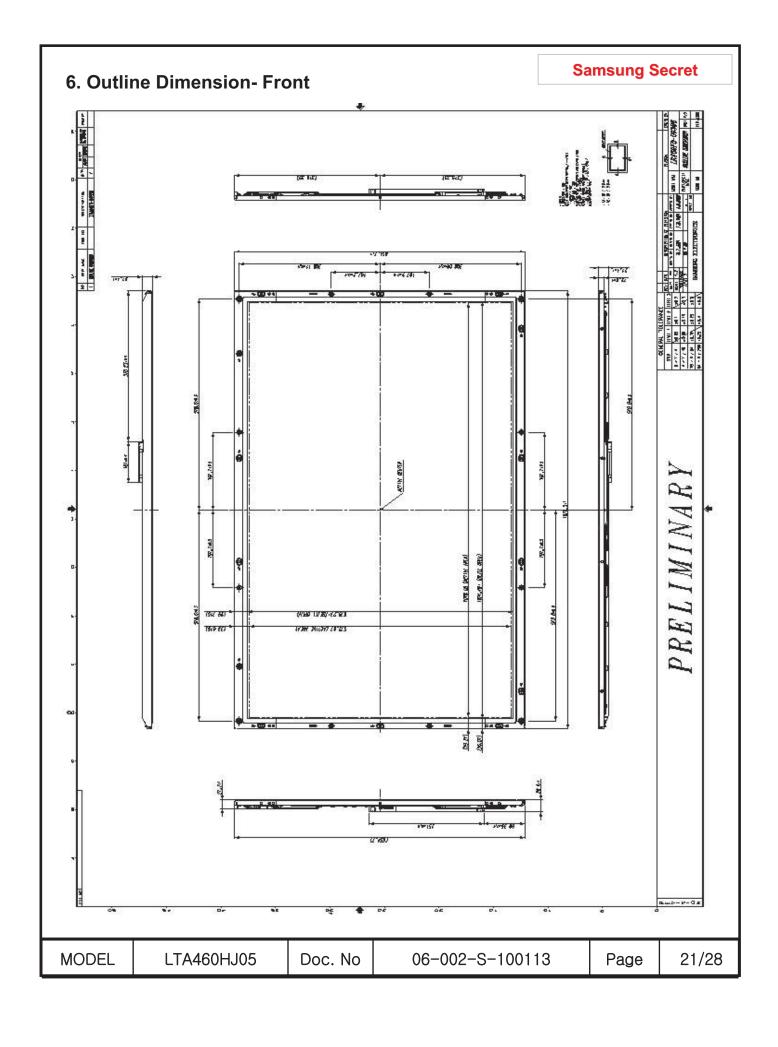
T4 : V_{DD} off time for Windows restart

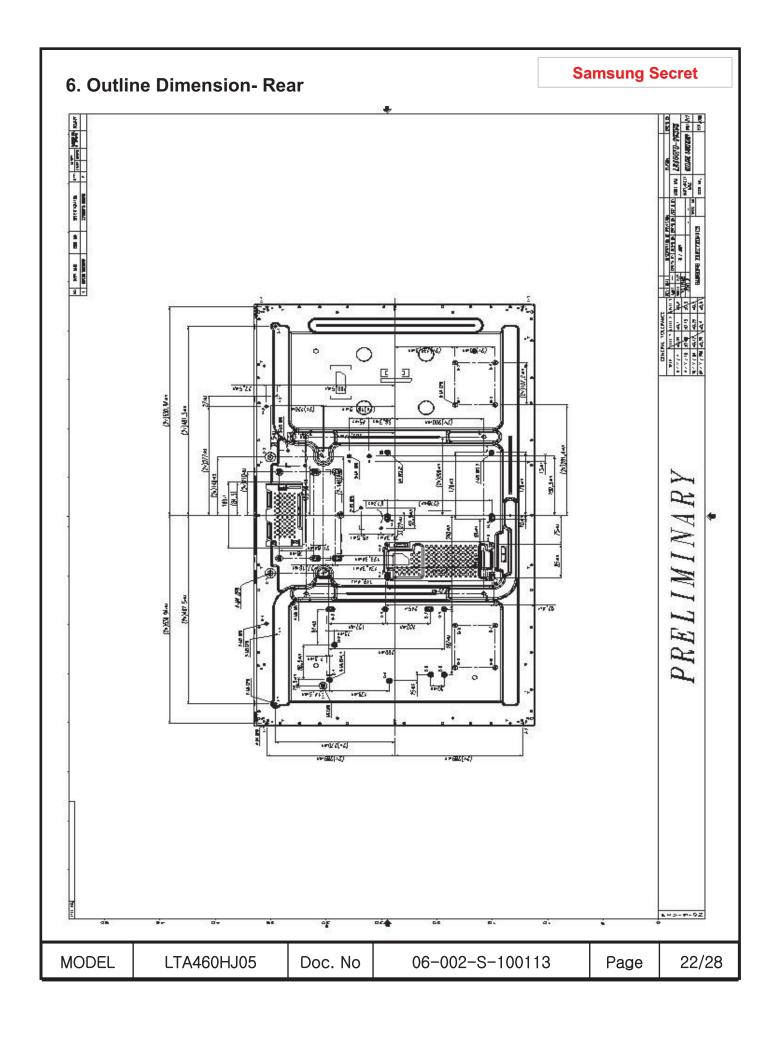
T5: The time from valid data to B/L enable at power ON.

T6: The time from valid data off to B/L disable at power Off.

- The supply voltage of the external system for the Module input should be the same as the definition of V_{DD}.
- Apply the lamp voltage within the LCD operation range. When the back light turns on before the LCD operation or the LCD turns off before the back light turns off, the display may momentarily show abnormal screen.
- In case of V_{DD} = off level, please keep the level of input signals low or keep a high impedance.
- T4 should be measured after the Module has been fully discharged between power off and on period.
- Interface signal should not be kept at high impedance when the power is on.
- In Case T5 is less than 1000msec and T6 is less than 100msec,
 Garbage Display can be seen. (It is not related to electrical function issue, Just for recommendation to prevent Garbage Display)

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7. Reliability Test

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Item	Test condition	Quantity
Temperature Step Stress	-20 \sim 60 $^{\circ}$ C, 40hr, 5 Cycle determination	4EA
HTOL	50℃, 500hr determination	4EA
LTOL	0°C, 500hr determination	4EA
HTS	70℃, 500hr determination	4EA
LTS	-30 °C, 500hr determination	4EA
THB	40 ℃ / 95%RH, 500hr determination	4EA
WHTS	60℃ / 75%RH, 250hr determination	4EA
Thermal Shock	-20 °C ~ 60 °C, 200cycle determination	4EA
ESD(operation)	contact : \pm 8 kV ,150 pF/330 Ω ,200Point,1 time/Point non-contact : \pm 15 kV,150 pF/330 Ω ,200Point,1 time/Point	3EA
Inverter Input Con. ESD	contact: ±2kV,150pF/330,Input Con.Pin,3 times/Pin	3EA
Vibration	10~300Hz/1.5G/10minSR, XYZ, 30min/axis	3EA
Shock	Half Sine, 11msec, \pm X,Y 40G, \pm Z 30G 1time/axis	3EA
PALLET Vibration	1.05Grms 5~200Hz 1hr	1PALLET(16EA)
PALLET Drop	4 edge 1face(bottom) 20 cm	1PALLET(16EA)

[Result Evaluation Criteria]

Under the display quality test conditions with normal operation state, these should be no change which may affect practical display functions.

* HTOL/ LTOL: High/Low Temperature Operating Life

*** THB : Temperature Humidity Bias

*** HTS/LTS : High/Low Temperature Storage

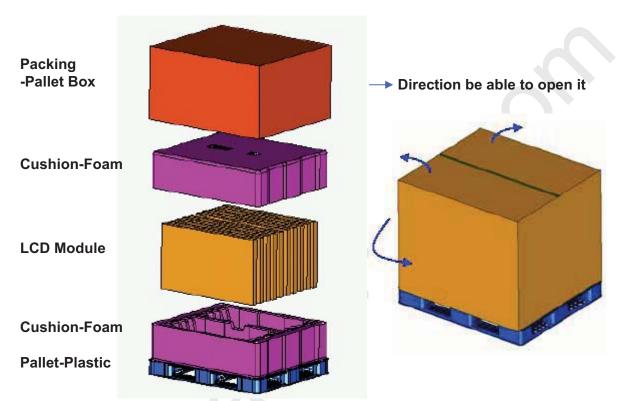
**** WHTS : Wet High Temperature Storage

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8. PACKING

- 8.1 CARTON (Internal Package)
- (1) Packing FormCorrugated fiberboard box and corrugated cardboard as shock absorber(2) Packing Mathed
- (2) Packing Method



8.2 Packing Specification

0.210	loking opoon	Toddott							
	Item	Spe	cification	Remark					
LCI) Packing		/ (Packing- llet Box)	1. 180.8 kg / LCD (16ea) 2. 12 Kg / Cushion-pallet (2 3. 8 Kg / Packing-Pallet Box 4. Cushion-pallet Material: 5. Packing-Pallet Box Material	k (1ea) EPS				
	Pallet	1Bc	x / Pallet	1. Pallet weight = 8.8kg					
Packi	ng Direction	\	/ertical						
Total	Pallet Size	H x '	V x height	1270mm(H) x 1150mm(V) x	x 844mm(h	eight)			
	Pallet 209.6 kg Pallet(8.8kg) + Module (180.8 kg) + Cushion (up + bottom =12kg) + Pallet-BOX(8kg)								
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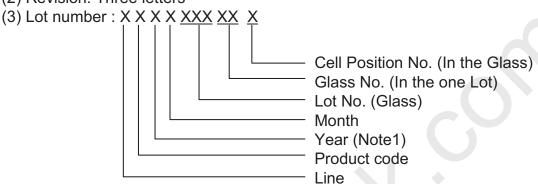
9. MARKING & OTHERS

Global LCD Panel Exchange Center

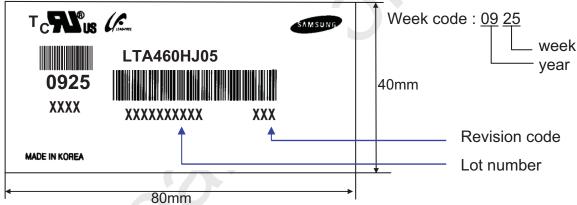
A nameplate bearing followed by is affixed to a shipped product at the specified location on each product.

(1) Part number: LTA460HJ05

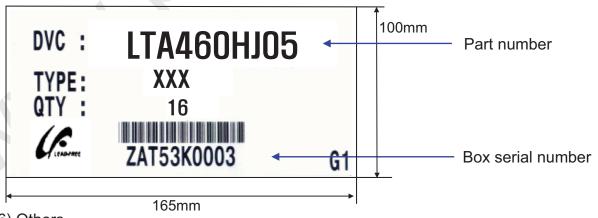
(2) Revision: Three letters



(4) Nameplate Indication



(5) Packing box attach



(6) Others

 After service part Lamps cannot be replaced because of the narrow bezel structure.

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10. General Precautions

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- 10.1 Handling
- (a) When the Module is assembled, it should be attached to the system firmly using all mounting holes. Be careful not to twist and bend the Module.
- (b) Because the inverter use high voltage, it should be disconnected from power before it is assembled or disassembled.
- (c) Refrain from strong mechanical shock and / or any force to the Module. In addition to damage, this may cause improper operation or damage to the Module and LED back light.
- (d) Note that polarizers are very fragile and could be damage easily. Do not press or scratch the surface harder than a HB pencil lead.
- (e) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, staining or discoloration may occur.
- (f) If the surface of the polarizer is dirty, clean it using absorbent cotton or soft cloth.
- (g) Desirable cleaners are water, IPA(Isopropyl Alcohol) or Hexane. Do not use Ketone type materials(ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
- (h) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs or clothes, it must be washed away with soap thoroughly.
- (i) Protect the module from Electrostatic discharge. Otherwise the ASIC IC or semiconductor would be damaged.
- (j) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (k) Do not disassemble the Module.
- (I) Do not disassemble shield case of inverter & LVDS board
- (m) Do not connect N.C pins. (Samsung internal use only)
- (n) Protection film for polarizer on the Module should be slowly peeled off just before use so that the electrostatic charge can be minimized. Must put on antistatic glove while handling a module
- (o) Pins of I/F connector should not be touched directly with bare hands.

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10.2 Storage

- (a) Do not leave the Module in high temperature, and high humidity for a long time. It is highly recommended to store the Module with temperature from 0 to $35\,^{\circ}$ C and relative humidity of less than 70%.
- (b) Do not store the TFT-LCD Module in direct sunlight.
- (c) The Module should be stored in a dark place. It is prohibited to apply sunlight or fluorescent light in storing.

10.3 Operation

- (a) Do not connect or disconnect the Module in the "Power On" condition.
- (b) Power supply should always be turned on/off by the "Power on/off sequence"
- (c) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference should be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- (d) The cable between the back light connector and its inverter power supply should be connected directly with a minimized length. A longer cable between the back light and the inverter may cause lower luminance of LED and may require higher startup voltage(Vs).

10.4 Operation Condition Guide

(a) The LCD product should be operated under normal conditions. Normal condition is defined as below;

- Temperature : 20±15 °C - Humidity : 55±20%

- Display pattern : continually changing pattern (Not stationary)

(b) If the product will be used in extreme conditions such as high temperature, humidity, display patterns or operation time etc.., It is strongly recommended to contact SEC for Application engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at Airports, Transit Stations, Banks, Stock market, and Controlling systems.

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10.5 Others

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- (a) Ultra-violet ray filter is necessary for outdoor operation.
- (b) Avoid condensation of water. It may result in improper operation or disconnection of electrode.
- (c) Do not exceed the absolute maximum rating value. (supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on)
 - Otherwise the Module may be damaged.
- (d) If the Module keeps displaying the same pattern for a long period of time, the image may be "sticked" to the screen. To avoid image sticking, it is recommended to use a screen saver.
- (e) This Module has its circuitry PCB's on the rear side and should be handled carefully in order not to be stressed.
- (f) Please contact SEC in advance when you display the same pattern for a long time.

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